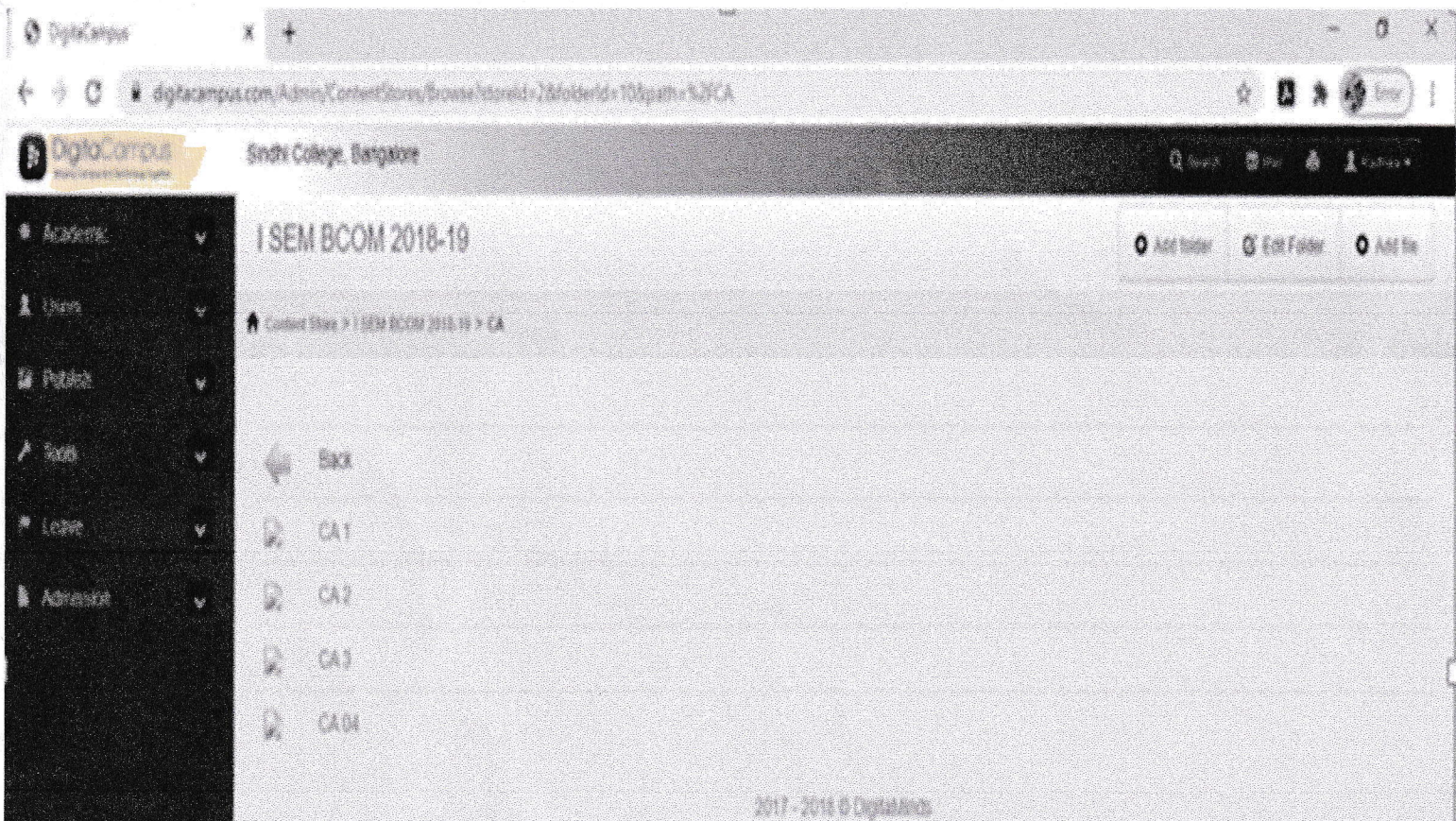
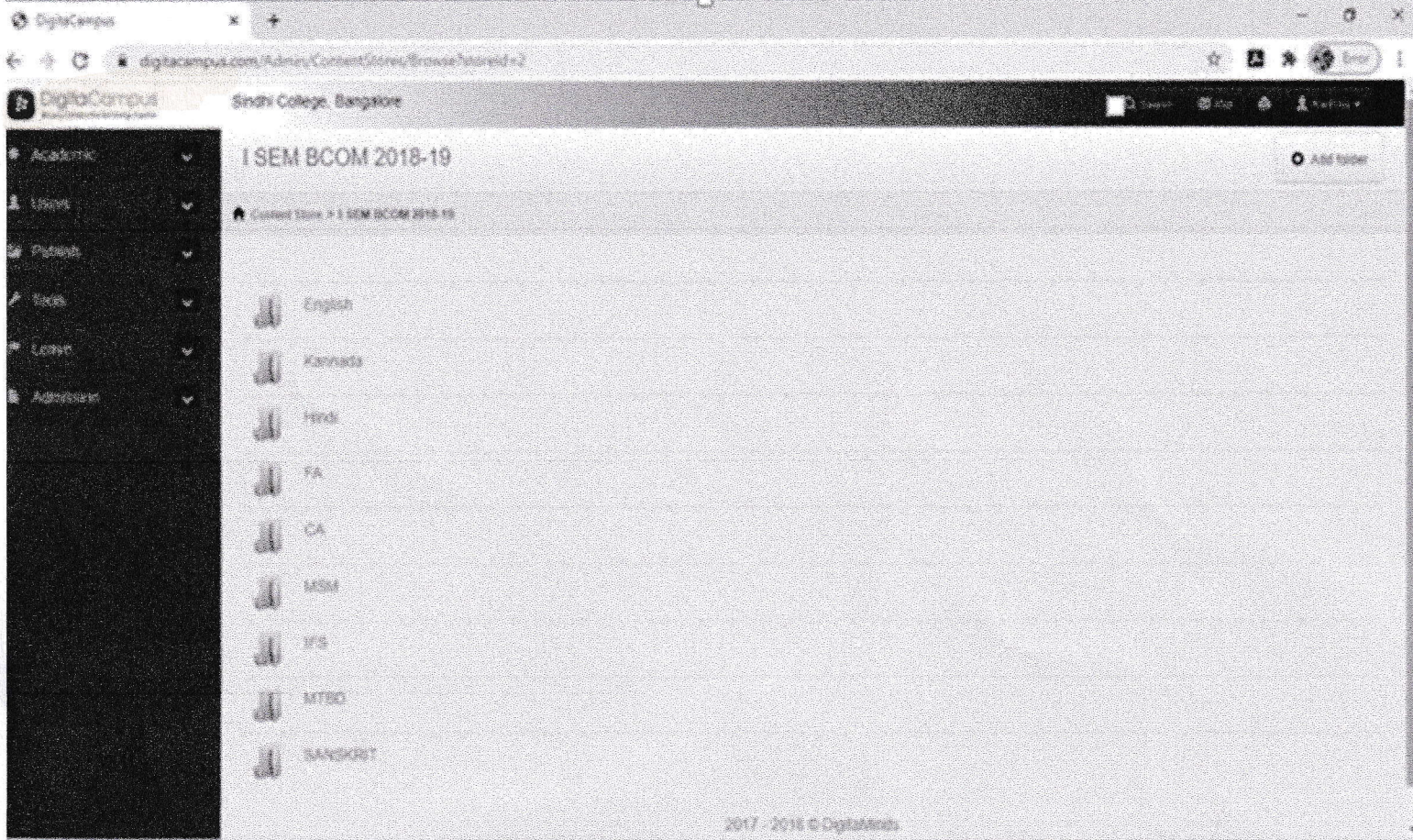




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Latest Updates

No latest updates.

Curriculum Coverage

Curriculum Topics Plan

Curriculum Topics Performance

No data.

Fee Dues (Till 1)

View all bills

No dues.

Library Dues

No dues.

Sections

Student Performance

Day Wise Lecture

II M.COM (FA)

SEM II

IV M.COM

SEM IV

Timetable

Print

Monday

#	Time	Section	Subject
1	10:30 AM to 11:30 AM	II M.COM (FA)	QTAE
		IV M.COM (FA)	GST
		IV M.COM	DTP

Tuesday

Wednesday

Thursday

Friday

Saturday

Sunday

Attendance

Today's

FN

AN

Today

Screen shot for having displayed faculty
time table,

Principal

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BCU
Options

3RD SEM BCOM 2019-20
BCU
Options

5TH SEM BCOM 2019-20
BCU
Options

REVISION CLASSES
FINAL YEAR
Options

FINAL YEAR BBA REVISION CLASSES
Hello Students, Hope Everyone are doing Good Revision classes for final year BBA subjects would be conducted in the college from 1st-10th September 2020 So all those students who have missed the prime classes or those who require revision in particular subjects should register their names with HOD/class teacher before 29th August 2020
Options

Calendar of Events - 2019-20
Options

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Calendar of Events

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Department of Computer Science and Electronics

← Back

Calendar Store > Calendar of Events - 2019-20 > Calendar of Events > Department of Computer Science and Electronics

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Calendar of Events... ▾

Show all X

Start Time table

Sindhi College, Bangalore

Akilandeswari-SCBU1052(2019-2020)

	7:30 AM to 8:30 AM	8:30 AM to 9:30 AM	9:30 AM to 10:30 AM	11:00 AM to 12:00 PM	12:00 PM to 1:00 PM	1:00 PM to 2:00 PM	2:00 PM to 3:00 PM
Monday	V BCA (CA)	I B.Sc (ELEC)					V B.Sc (MP LAB)
Tuesday	V BCA (CA)		I B.Sc (ELEC)	V B.Sc (COM1)			
Wednesday		V BCA (CA)	V B.Sc (COM1)		V B.Sc (COM1 LAB)		
Thursday	V BCA (MP LAB)	V B.Sc (COM1)	I B.Sc (ELEC)		I B.Sc (ELEC LAB)		
Friday				V BCA (MP LAB)	I B.Sc (ELEC)		
Saturday		V B.Sc (COM1)	V BCA (CA)				

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Bengaluru - 560 024.

Start Time table

Sindhi College, Bangalore

Roopa Rajasekar A-
SCBU1034(2019-2020)

	7:30 AM to 8:30 AM	8:30 AM to 9:30 AM	9:30 AM to 10:30 AM	11:00 AM to 12:00 PM	12:00 PM to 1:00 PM	1:00 PM to 2:00 PM
Monday		I BBAM(Qtm)	III BCOM A(QABD)	I BSc(Mat)		
Tuesday	I BBAM(Qtm)					I BSc(Mat LAB)
Wednesday	I BSc(Mat)		III BCOM A(QABD)	I BBAM(Qtm)	I BSc(Mat LAB)	
Thursday	III BCOM A(QABD)			I BSc(Mat)		
Friday	I BSc(Mat)	III BCOM A(QABD)	I BBAM(Qtm)			I BSc(Mat LAB)
Saturday	I BBAM(Qtm)	III BCOM A(QABD)	I BSc(Mat)			

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**SINDHI COLLEGE**

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Department of Computer Science**Calendar of Events BCA& BSc 2019 - 20**

Sl.No	EVENTS	Tentative date
1	Guest Lecture - CS	25-07-2019
2	PPT presentation Competition	10-08-2019
3	Electronics Guest Lecture	23-08-2019
4	Electronics Event	12-09-2019
5	IT Debate	14-09-2019
6	Guest Lecture - CS	28-09-2019
7	Guest Lecture - CS	25-01-2020
8	Web designing Competition	08-02-2020
9	Electronics Workshop	13 and 14th Feb 2020
10	Industrial Visit - I yr BCA	20-03-2020
11	Industrial Visit - II yr BCA	21-03-2020
12	National Science Day Celebrations	28-02-2020
13	Industrial Visit - III yr BCA	06-03-2020
14	Guest Lecture - CS	13-03-2020
15	Electronics Guest Lecture	18-03-2020
16	Group discussion	20-03-2020

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Time table

Sindhi College, Bangalore

II BCOM A (2019-2020)

	7:30 AM to 8:30 AM	8:30 AM to 9:30 AM	9:30 AM to 10:30 AM	11:00 AM to 12:00 PM	12:00 PM to 1:00 PM	1:00 PM to 2:00 PM
Mon	MTBD Roopa R A	Eng Nagarathna	MM Grace,J	KAN/Hin/San Kariyanna.S,S Dr./Girirajkumar,Dr./Subrahmanya,N R	AFA Keerthana ,H	,
Tue	MTBD Roopa R A	KAN/Hin/San Kariyanna.S,S Dr./Girirajkumar,Dr./Subrahmanya,N R	BOI Srinidhi	AFA Keerthana ,H	EPH Srinidhi	Eng Nagarathna ,
Wed	MM Grace,J	KAN/Hin/San Kariyanna.S,S Dr./Girirajkumar,Dr./Subrahmanya,N R	MTBD Roopa R A	Eng Nagarathna	SD Bhavya	BOI Srinidhi
Thu	MM Grace,J	AFA Keerthana ,H	BOI Srinidhi	EPH Srinidhi	Eng Nagarathna	SD Bhavya
Fri	AFA Keerthana ,H	MTBD Roopa R A	MM Grace,J	BOI Srinidhi	KAN/Hin/San Kariyanna.S,S Dr./Girirajkumar,Dr./Subrahmanya,N R	
Sat	MM Grace,J	BOI Srinidhi	AFA Keerthana ,H	SD Bhavya		

Class time table downloaded from Digi Campus.


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Curriculum Topics Plan

SECTION: I BSC

SUBJECT: Electronics

Akilandeswari, .

FROM: 08/07/2019

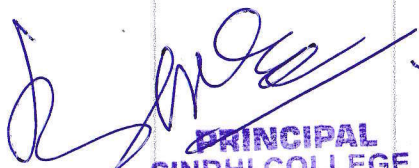
TO: 31/10/2019

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Name	Section	Subject	Task	Status	Start Date	End Date	Progress
Akilandeswari, .	I BSC	Electronics	Filters– types, circuit diagram and explanation of shunt capacitor filter with waveforms. Zener diode regulator– circuit diagram and explanation for load and line regulation	Completed	27-08-2019 12:00 AM	31-08-2019 11:59 PM	100%
			Fixed bias(base bias) without and with RE, collector to base bias, voltage divider bias and emitter bias (+VCC and –VEE bias) –circuit diagrams and their working, Q point expressions for voltage divider biasing only with numerical problems. Transistor as a switch – circuit and working. Darlington pair and its applications	Completed	23-09-2019 12:00 AM	27-09-2019 11:59 PM	100%
			FWR- center tapped and bridge FWR	Completed	19-08-2019 12:00 AM	22-08-2019 11:59 PM	100%
			leakage currents(mention only), numerical problems. Study of CE Characteristics - different regions. Experimental circuit and procedure. Study of CB Characteristics - different regions, Base width modulation- Early effect.	Completed	11-09-2019 12:00 AM	14-09-2019 11:59 PM	100%
			superposition theorem ☐ statements and steps involved, reciprocity theorem ☐ statement, maximum power transfer theorem-derivation	Completed	30-07-2019 12:00 AM	02-08-2019 11:59 PM	100%
			AC applied to Series and parallel RLC circuit	Completed	19-07-2019 12:00 AM	19-07-2019 11:59 PM	100%

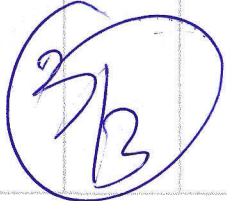
This document consists of three pages

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2/3

Binary, hexadecimal – conversion from binary to decimal and vice-versa, binary to hexadecimal and vice-versa	Completed	03-10-2019 12:00 AM	04-10-2019 11:59 PM	100%
Bipolar Junction Transistor: Construction, principle & working of NPN transistor, terminology.	Completed	04-09-2019 12:00 AM	06-09-2019 11:59 PM	100%
Configuration – CE, CB, CC (mention only). Definition of α , β and γ and their interrelations	Completed	09-09-2019 12:00 AM	09-09-2019 11:59 PM	100%
decimal to hexadecimal and vice versa, addition and subtraction of binary numbers and hexadecimal numbers, Subtraction using 2's complement	Completed	14-10-2019 12:00 AM	18-10-2019 11:59 PM	100%
Disadvantages of Zener diode regulator. Transistor series regulator – circuit diagram and working.	Completed	03-09-2019 12:00 AM	03-09-2019 11:59 PM	100%
Hybrid parameters – definitions of h_{ie} , h_{oe} , h_{fe} and h_{re} Transistor biasing – need for biasing, DC load line, operating point, thermal runaway, stability and stability factor (mention the equation-no derivation). Different types of biasing	Completed	16-09-2019 12:00 AM	18-09-2019 11:59 PM	100%
Junction Field Effect Transistor (JFET)	Completed	30-09-2019 12:00 AM	30-09-2019 11:59 PM	100%
Norton's theorem and interconversion	Completed	29-07-2019 12:00 AM	29-07-2019 11:59 PM	100%
Number System -Introduction	Completed	01-10-2019 12:00 AM	01-10-2019 11:59 PM	100%
Numerical problems on all theorems	Completed	05-08-2019 12:00 AM	05-08-2019 11:59 PM	100%
Rectifier:HWR	Completed	16-08-2019 12:00 AM	16-08-2019 11:59 PM	100%
Review of Ohms law, Kirchhoff's laws, voltage divider and current divider theorems, open and short circuits. Thevenin's theorem	Completed	23-07-2019 12:00 AM	27-07-2019 11:59 PM	100%
Review of passive components – R, L & C Voltage and current	Completed	08-07-2019 12:00 AM	13-07-2019 11:59 PM	100%

	Review of PN junction diode and characteristics, ideal diode and diode approximations – representations. Block diagram of a Regulated Power Supply	Completed	06-08-2019 12:00 AM	10-08-2019 11:59 PM	100%
	Series RL circuit excited by DC source	Completed	17-07-2019 12:00 AM	17-07-2019 11:59 PM	100%
	signed number arithmetic – addition. Types of codes–BCD code, gray code, gray to binary conversion and vice versa, excess – 3 Code - self complementing property	Completed	21-10-2019 12:00 AM	26-10-2019 11:59 PM	100%
	Transformer: Principle, construction and working ,Switches	Completed	22-07-2019 12:00 AM	22-07-2019 11:59 PM	100%
	Transient analysis of RC and RL circuits	Completed	15-07-2019 12:00 AM	16-07-2019 11:59 PM	100%



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